DATASHEET

2/1/2013

Fema Part Number

GM800480X-70-TTX3NLW-H	
Description	7.0" Full Color TFT Display
	800x480 Resolution
	Brightness = 1000 nits (Typical)
	Optional Resistive Touch Panel Available

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1. General Specifications

No.	*Item*	*Specification*	*Remark*
1	LCD size	7.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	800 × 3(RGB) × 480	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.0642(W) × 0.1790(H) mm	
6	Active area	154.08(W) × 85.92(H) mm	
7	Module size	164.9(W) × 100.0(H) × 5.7(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight power consumption	3.6W @ 1,000 nits 1.7W @ 500 nits	
12	Panel power consumption	0.226W (Typ.)	
13	Weight	150 g (Typ.)	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

2.1. TFT LCD Panel Driving Section

1. FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.
2. LED Light Bar Connector is used for the integral backlight system. The recommended model is BHSR-02VS-1 manufactured by JST.

Pin No	*Symbol*	*I/O*	*Function*	*Remark*
1	V_{LED+}	P	Power for LED backlight (Anode)	
2	V_{LED+}	P	Power for LED backlight (Anode)	
3	V_{LED-}	P	Power for LED backlight (Cathode)	
4	V_{LED-}	P	Power for LED backlight (Cathode)	
5	GND	P	Power ground	
6	V_{COM}	I	Common voltage	
7	DV_{DD}	P	Power for Digital circuit	
8	MODE	I	DE/SYNC mode select	Note 3
9	DE	I	Data Input Enable	
10	VS	I	Vertical Sync Input	
11	HS	I	Horizontal Sync Input	
12	B7	I	Blue data (MSB)	
13	B6	I	Blue data	
14	B5	I	Blue data	
15	B4	I	Blue data	
16	B3	I	Blue data	
17	B2	I	Blue data	
18	B1	I	Blue data	
19	B0	I	Blue data (LSB)	
20	G7	I	Green data (MSB)	
21	G6	I	Green data	
22	G5	I	Green data	
23	G4	I	Green data	
24	G3	I	Green data	
25	G2	I	Green data	
26	G1	I	Green data	
27	G0	I	Green data (LSB)	

28	R7	I	Red data (MSB)	
29	R6	I	Red data	
30	R5	I	Red data	
31	R4	I	Red data	
32	R3	I	Red data	
33	R2	I	Red data	
34	R1	I	Red data	
35	R0	I	Red data (LSB)	
36	GND	P	Power ground	
37	DCLK	I	Sample clock	
38	GND	P	Power ground	
39	L/R	I	Right/Left selection	Note 2,5
40	U/D	I	Up/down selection	Note 2,5
41	V_{GH}	P	Gate ON voltage	
42	V_{GL}	P	Gate OFF voltage	
43	AV_{DD}	P	Power for Analog circuit	
44	RESET	I	Global reset pin.	Note 1
45	NC		No connection	
46	V_{COM}	I	Common voltage	
47	DITHB	I	Dithering function	Note 4
48	GND	P	Power ground	
49	NC		No connection	
50	NC		No connection	

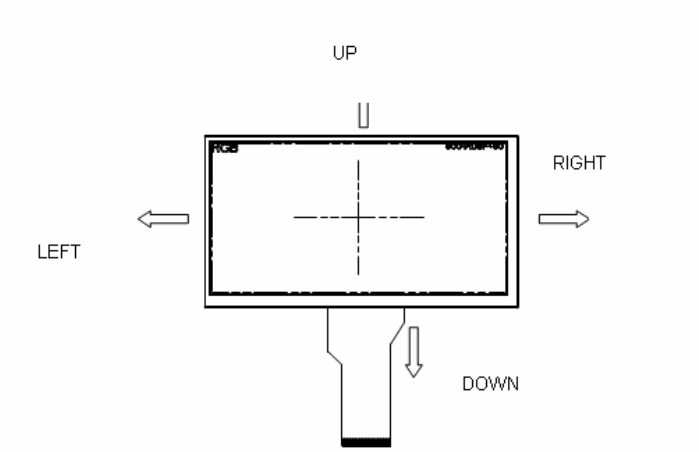
I: input, O: output, P: Power

Note 1: Global reset pin. Active Low to enter Reset State. Suggest to connecting with an RC reset circuit for stability. Normally pull high.

Note 2: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	R/L	
GND	V_{DD}	Up to down, left to right
V_{DD}	GND	Down to up, right to left
GND	GND	Up to down, right to left
V_{DD}	V_{DD}	Down to up, left to right

Note 3: DE/SYNC mode select, Normally pull high.
H: DE mode.
L: HS/VS mode.
Note4: Dithering function enable control. Normally pull high.
DITHB=”1”,Disable internal dithering function. For 18bit RGB interface, connect two LSB bits of all the R/G/B data buses to GND.
DITHB=”0”,Enable internal dithering function, For TTL 24bit parallel RGB image data input.
Note 5: Definition of scanning direction.
Refer to the figure as below:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high.
When DITHB=”1”,Disable internal dithering function, When DITHB=”0”,Enable internal dithering function,

2.2. Backlight Unit Section

Pin No.	***Symbol***	***I/O***	***Function***	***Remark***
1	V_{LED+}	P	Power for LED backlight anode	Red
2	V_{LED-}	P	Power for LED backlight cathode	Black

3. Operation Specifications

3.1. Absolute Maximum Rating

(GND=AV_{SS}=0V, Note 1)

Item	***Symbol***	***Values***		***Unit***	***Remark***
		Min.	***Max.***		
Power voltage	DV_{DD}	-0.3	5.0	V	
	AV_{DD}	-0.5	13.5	V	
	V_{GH}	-0.3	40.0	V	
	V_{GL}	-20.0	-0.3	V	
	V_{GH}-V_{GL}	-	40.0	V	
Operation temp.	T_{OP}	-30	85	℃	
Storage temp.	T_{ST}	-30	85	℃	
LED Reverse Voltage	V_R	-	1.2	V	each LED(Note 2)
LED Forward Current	I_F	-	80	mA	each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

3.1.1 Typical Operation Condition

(GND=AV_{SS}=0V, Note 1)

Item	***Symbol***	***Values***			***Unit***	***Remark***
		Min.	***Typ.***	***Max.***		
Power voltage	DV_{DD}	3.0	3.3	3.6	V	Note 2
	AV_{DD}	10.2	10.4	10.6	V	
	V_{GH}	15.3	16.0	16.7	V	
	V_{GL}	-7.7	-7.0	-6.3	V	
Input signal voltage	V_{COM}	3.8	4.0	4.2	V	
Input logic high voltage	V_{IH}	$0.7V_{DD}$	-	V_{DD}	V	Note 3
Input logic low voltage	V_{IL}	0	-	$0.3V_{DD}$	V	

Note 1: Be sure to apply V_{CC} and V_{GL} to the LCD first, and then apply V_{GH}.
Note 2: V_{CC} setting should match the signals output voltage (refer to Note 3) of customer's system board .
Note 3: DCLK,HS,VS,RSTB,UPDN,STLR,MODE,DITHB.

3.1.2 Current Consumption

(GND=AV_{SS}=0V)

Item	***Symbol***	***Values***			***Unit***	***Remark***
		Min.	***Typ.***	***Max.***		
Current for Driver	I_{GH}	-	0.2	1.0	mA	V_{GH} =16.0V
	I_{GL}	-	0.2	1.0	mA	V_{GL} = -7.0V
	I_{CC}	-	5.5	10.0	mA	V_{CC} =3.3V
	IAV_{DD}	-	32.0	50.0	mA	AV_{DD} =10.4V

3.1.3. Backlight Driving Conditions

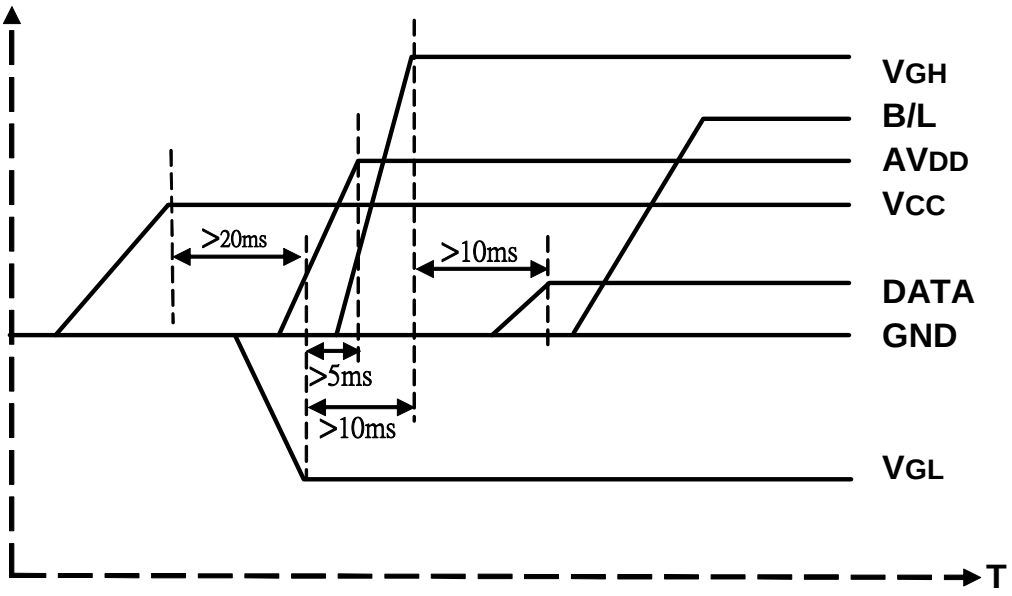
Item	*Symbol*	*Values*			*Unit*	*Remark*
		Min.	*Typ.*	*Max.*		
Voltage for LED backlight	V_L		9.9	10.5	V	Note 1
Current for LED backlight	I_L		360	560	mA	
LED life time	-	30K	40K	-	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25℃ and I_L =360mA.

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and I_L =360mA. The LED lifetime could be decreased if operating I_L is lager than 360 mA. The LED lifetime could be increased if operating I_L is smaller than 360 mA.

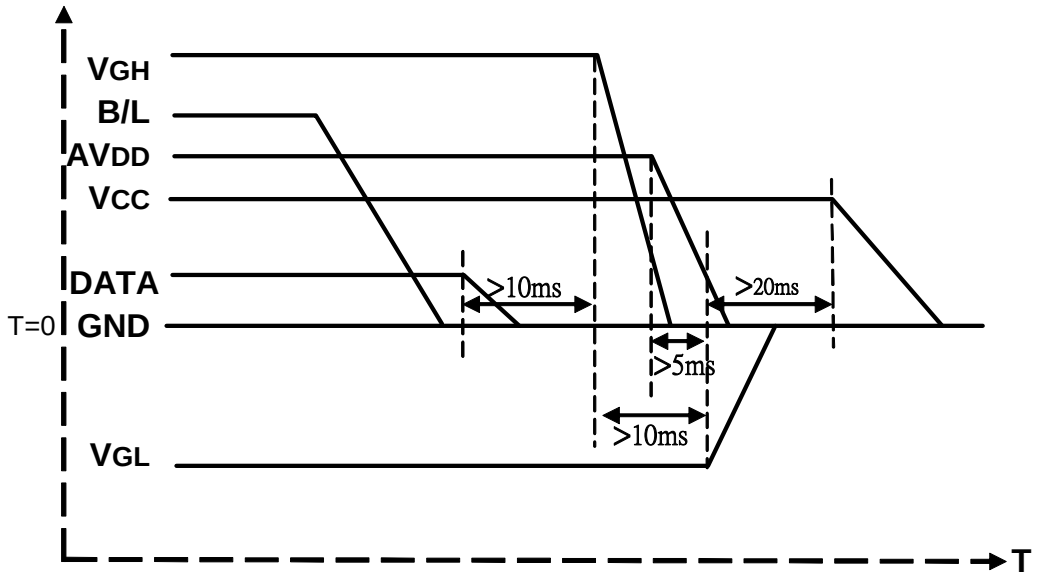
3.2 Power Sequence

3.2.1. Power on:



V_{CC}→V_{GL}→V_{GH}→Data→B/L

3.2.2. Power off:



B/L→Data→V_{GH}→V_{GL}→V_{CC}

Note: Data include R0~R5, B0~B5, GO~G5, STLR,UPDN, DCLK, HS,VS,DE.

3.3 Timing Characteristics

3.3.1 AC Electrical Characteristics

Item	***Symbol***	***Values***			***Unit***	***Remark***
		Min.	***Typ.***	***Max.***		
HS setup time	T_{hst}	8	-	-	Ns	
HS hold time	T_{hhd}	8	-	-	Ns	
VS setup time	T_{vst}	8	-	-	Ns	
VS hold time	T_{vhd}	8	-	-	Ns	
Data setup time	T_{dsu}	8	-	-	Ns	
Data hole time	T_{dhd}	8	-	-	Ns	
DE setup time	T_{esu}	8	-	-	Ns	
DE hole time	T_{ehd}	8	-	-	Ns	
VDD Power On Slew rate	T_{POR}	-	-	20	ms	
RSTB pulse width	T_{Rst}	10	-	-	us	
CLKIN cycle time	T_{coh}	20	-	-	Ns	
CLKIN pulse duty	T_{cwh}	40	50	60	%	
Output stable time	T_{sst}	-	-	6	us	

3.3.2 Timing

Item	***Symbol***	***Values***			***Unit***	***Remark***
		Min	***Typ***	***Max***		
Horizontal Display Area	Thd	-	800	-	DCLK	
DCLK Frequency	Fclk	26.4	33.3	46.8	MHz	
One Horizontal Line	Th	862	1056	1200	DCLK	
HS pulse width	Thpw	1	-	40	DCLK	
HS Back Porch(Blanking)	Thb	46	46	46	DCLK	
HS Front Porch	thfp	16	210	354	DCLK	

Item	***Symbol***	***Values***			***Unit***	***Remark***
		Min	***Typ***	***Max***		
Vertical Display Area	tvd	-	480	-	TH	
VS period time	tv	510	525	650	TH	
VS pulse width	tvpw	1	-	20	TH	
VS Back Porch(Blanking)	tvb	23	23	23	TH	
VS Front Porch	tvfp	7	22	147	TH	

3.3.3. Timing Diagram



Figure 3. 1 Horizontal input timing diagram.

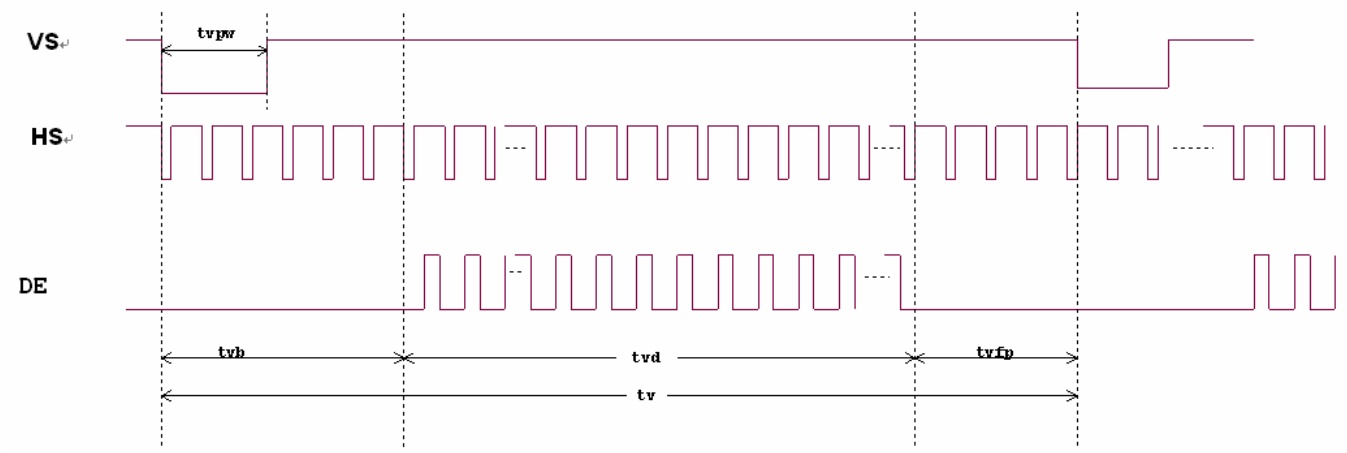


Figure 3. 2 Vertical input timing diagram.

4. Optical Specifications

Item	*Symbol*	*Condition*	*Values*			*Unit*	*Remark*
			Min.	*Typ.*	*Max.*		
View angle (CR≧10)	θ_L	$\Phi=180^o$(9 o'clock)	60	70	-	degree	Note 1
	θ_R	$\Phi=0^o$(3 o'clock)	60	70	-		
	θ_T	$\Phi=90^o$(12 o'clock)	40	50	-		
	θ_B	$\Phi=270^o$(6 o'clock)	60	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^o$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5 Note 6
	W_Y		0.28	0.33	0.38	-	
Luminance			800	1000	-	Cd/m^2	Note 6
Uniformity			-	70	-	%	Note 7

Test Conditions:

1. V_{CC}=3.3V, I_L=400mA (Backlight current), the ambient temperature is 25℃.
2. The test systems refer to Note 2.

5. Reliability Test Items

Item	*Test Conditions*	*Remark*
High Temperature Storage	Ta = 85℃ 240hrs	Note 1, 4
Low Temperature Storage	Ta = -30℃ 240hrs	Note 1, 4
High Temperature Operation	Ts = 85℃ 240hrs	Note 2, 4
Low Temperature Operation	Ta = -30℃ 240hrs	Note 1, 4
Operate at High Temperature and Humidity	+40℃, 90%RH 240hrs	Note 4
Thermal Shock	-30℃/30 min ~ +80℃/30 min for a total 100 cycles, Start with cold temperature and end with high temperature.	Note 4
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X. Y. Z. (6 hours for total)	
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 times for each direction	
Electro Static Discharge	± 2KV, Human Body Mode, 100pF/1500Ω	

Note 1: Ta is the ambient temperature of samples.
Note 2: Ts is the temperature of panel's surface.
Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

1. Store the module in a dark room where must keep at 25±10℃ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

7. Mechanical Drawing

